

In one embodiment, the invention is an apparatus. The apparatus includes a first drain bias network having an input suitable to couple to a FLASH cell. The apparatus also includes a second drain bias network having an input suitable to couple to a FLASH cell. The apparatus further includes an equalization circuit having a first node coupled to the input of the first drain bias network and having a second node coupled to the input of the second drain bias network and having a control signal to control operation of the equalization circuit.